

1/13

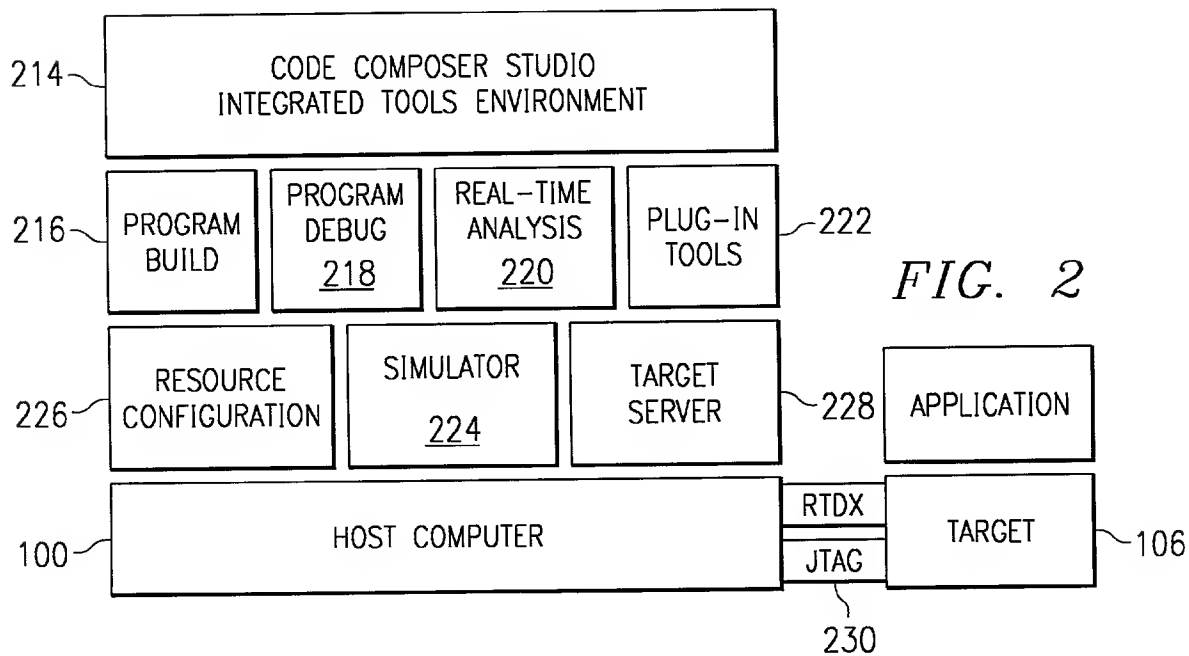
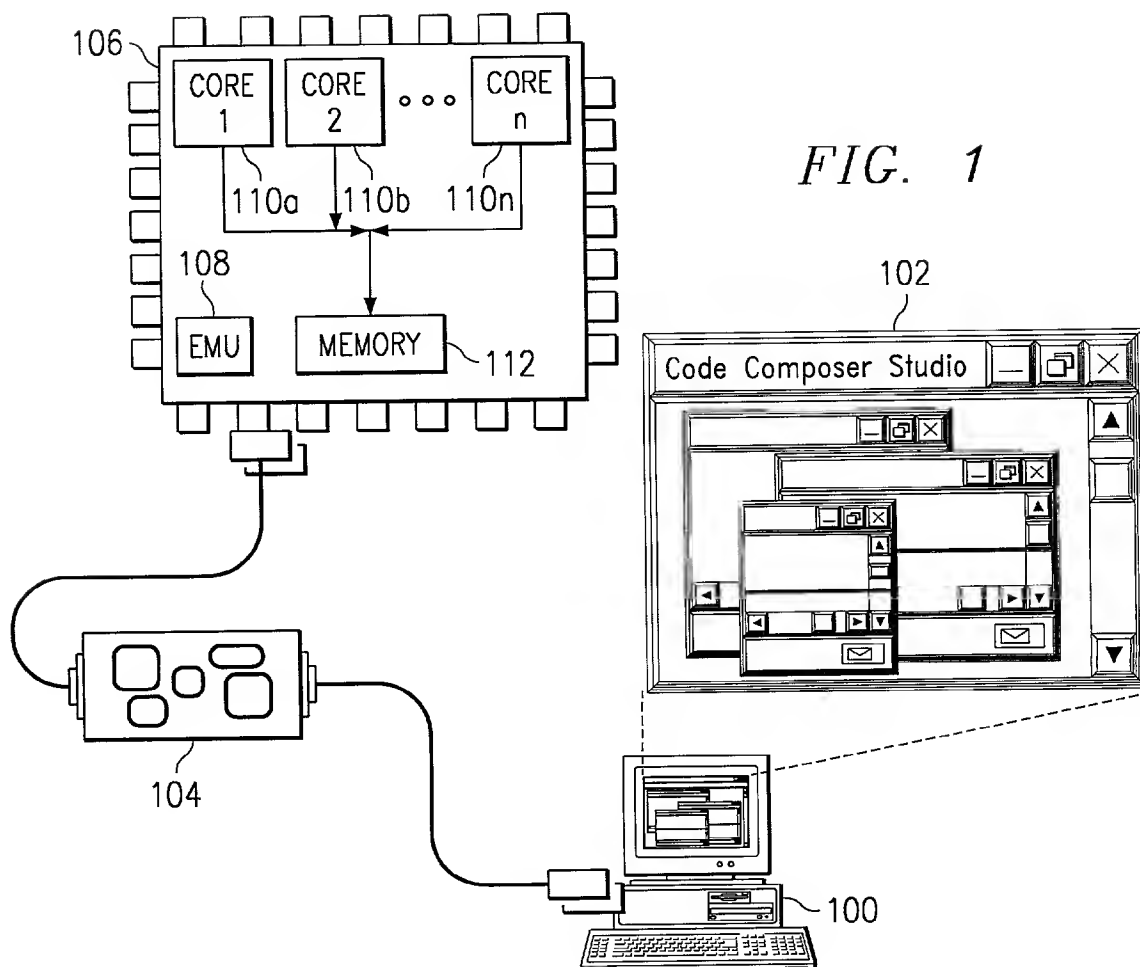
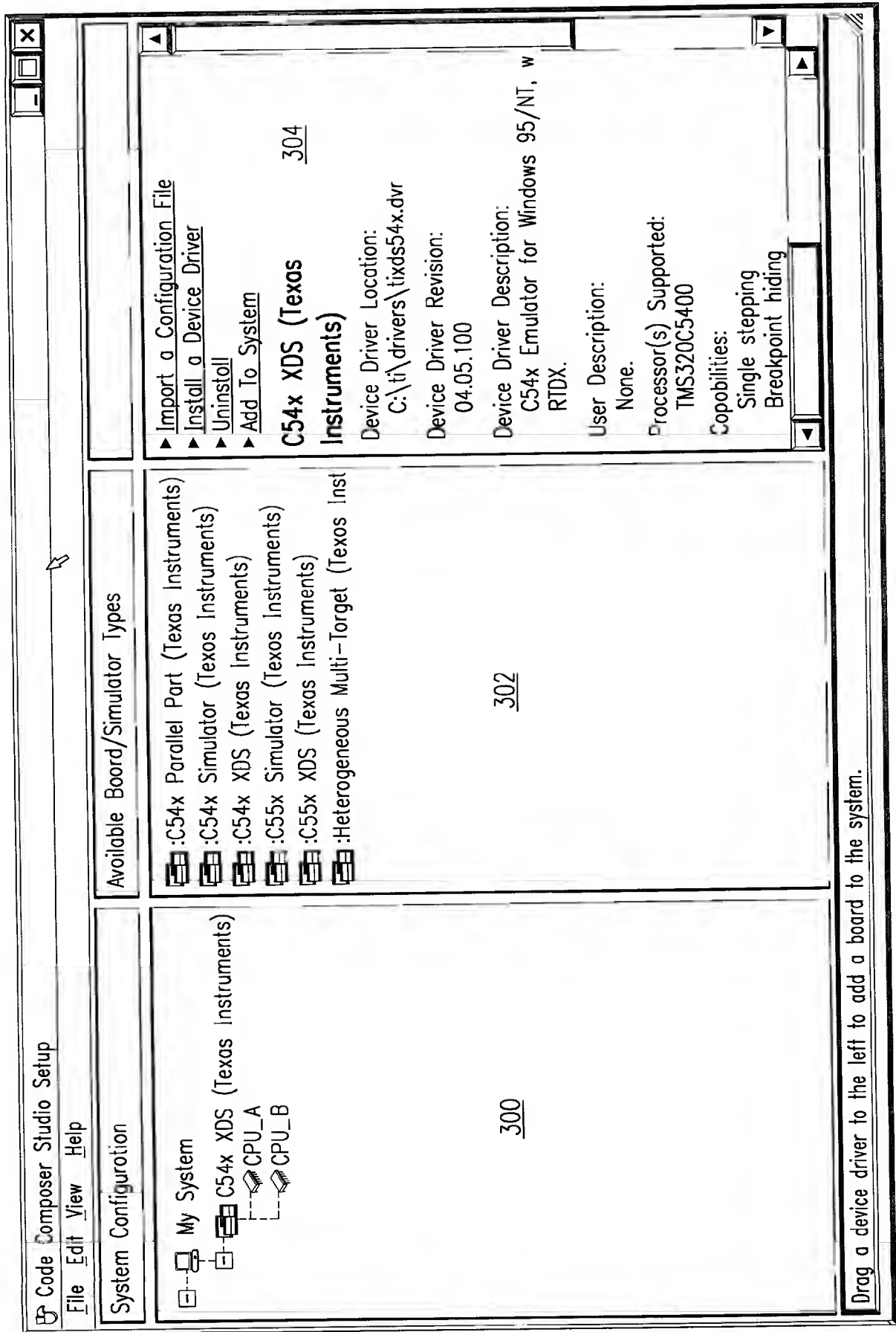


FIG. 3



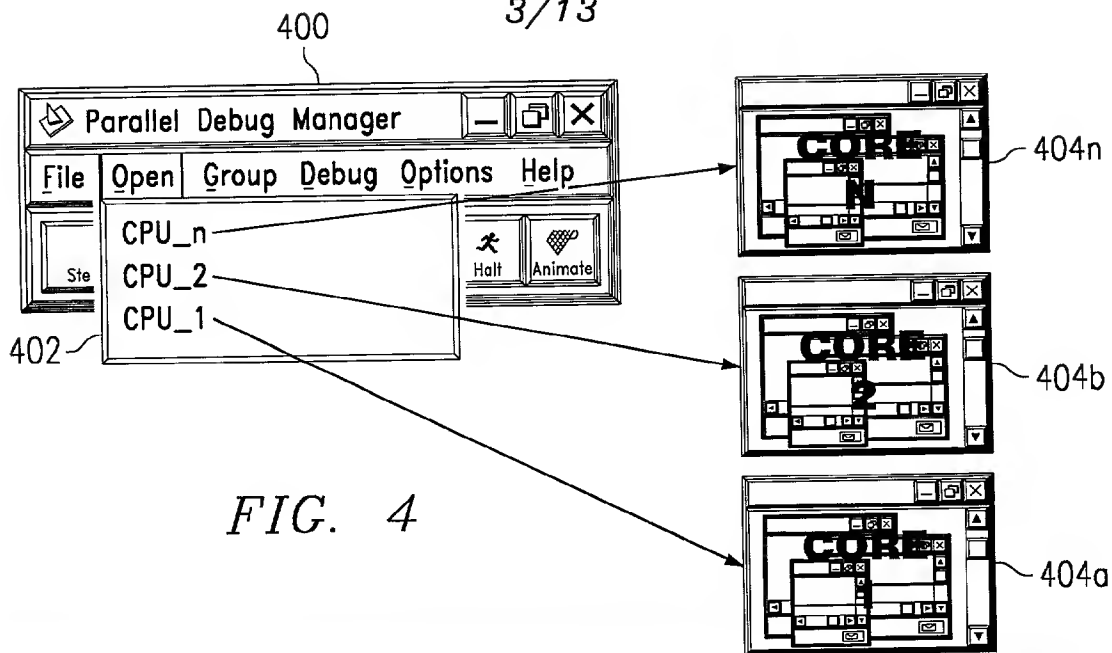


FIG. 4

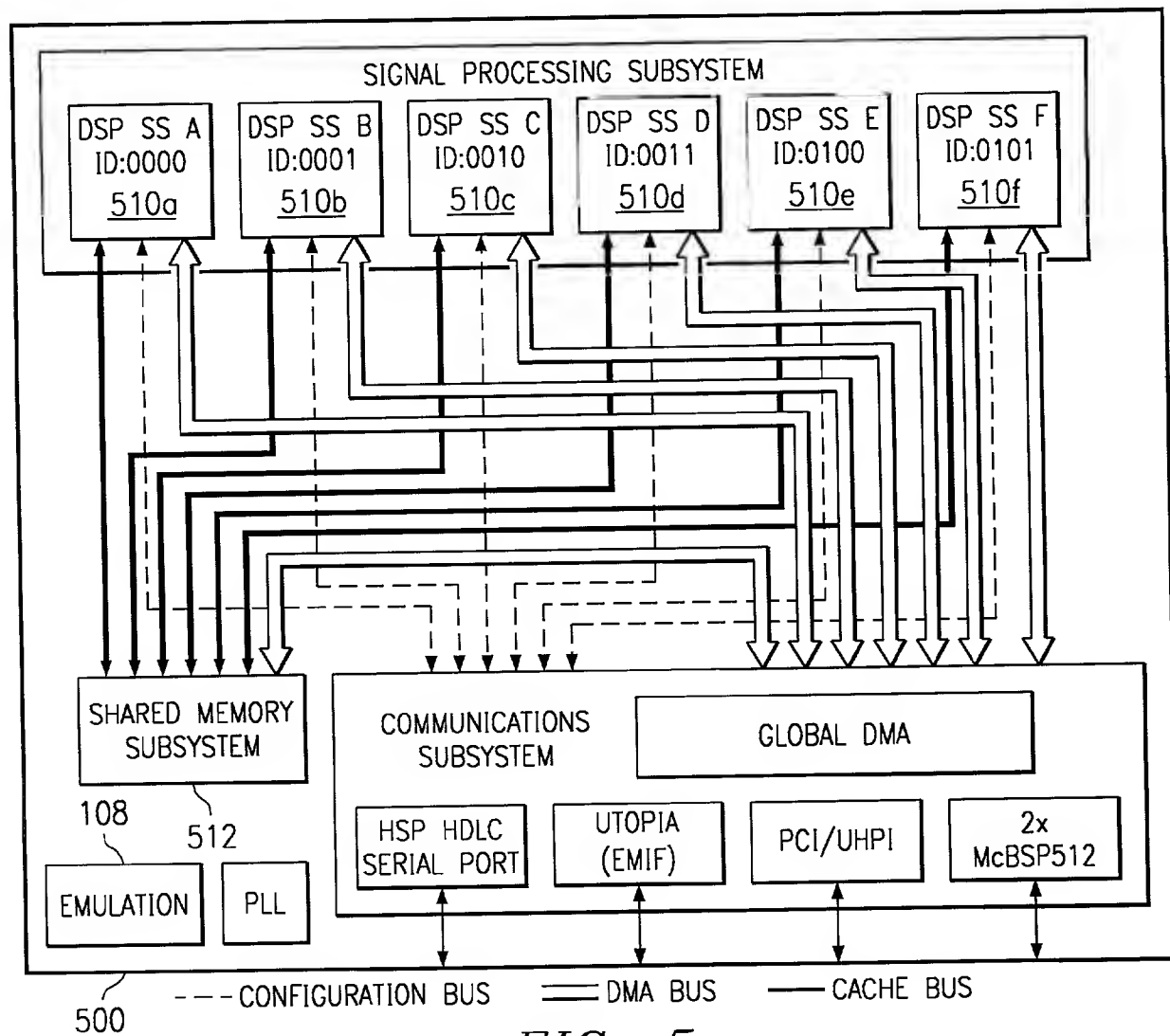


FIG. 5

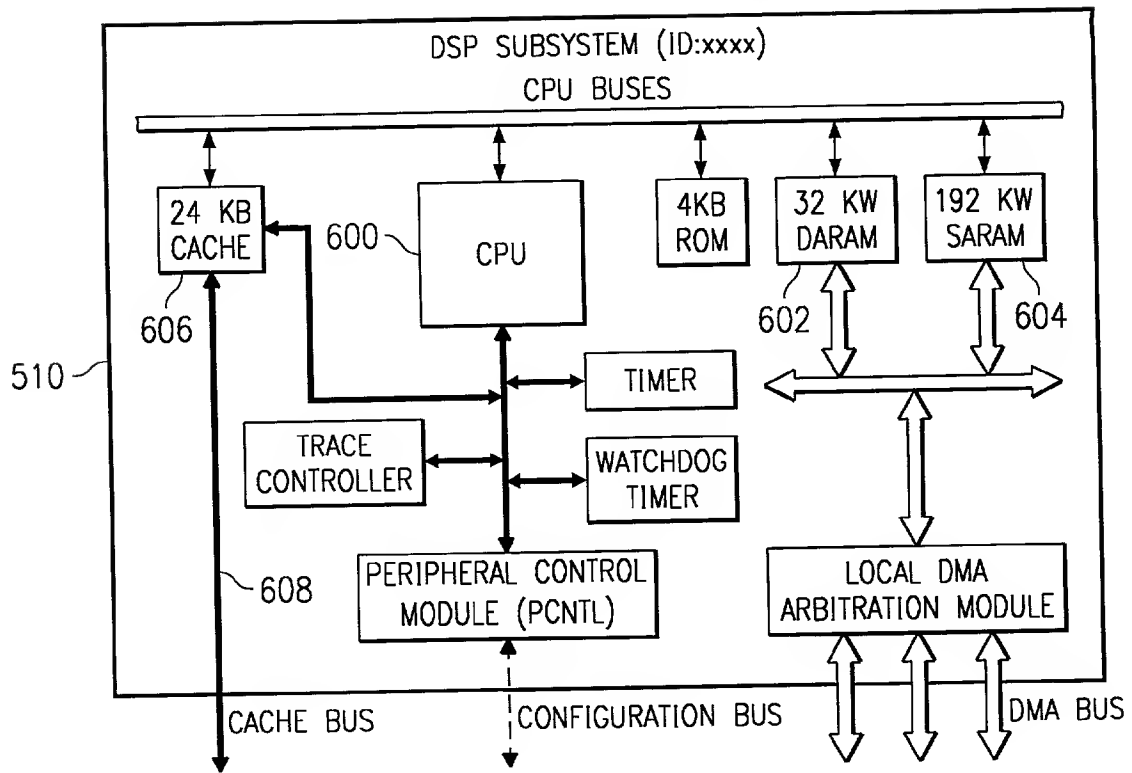
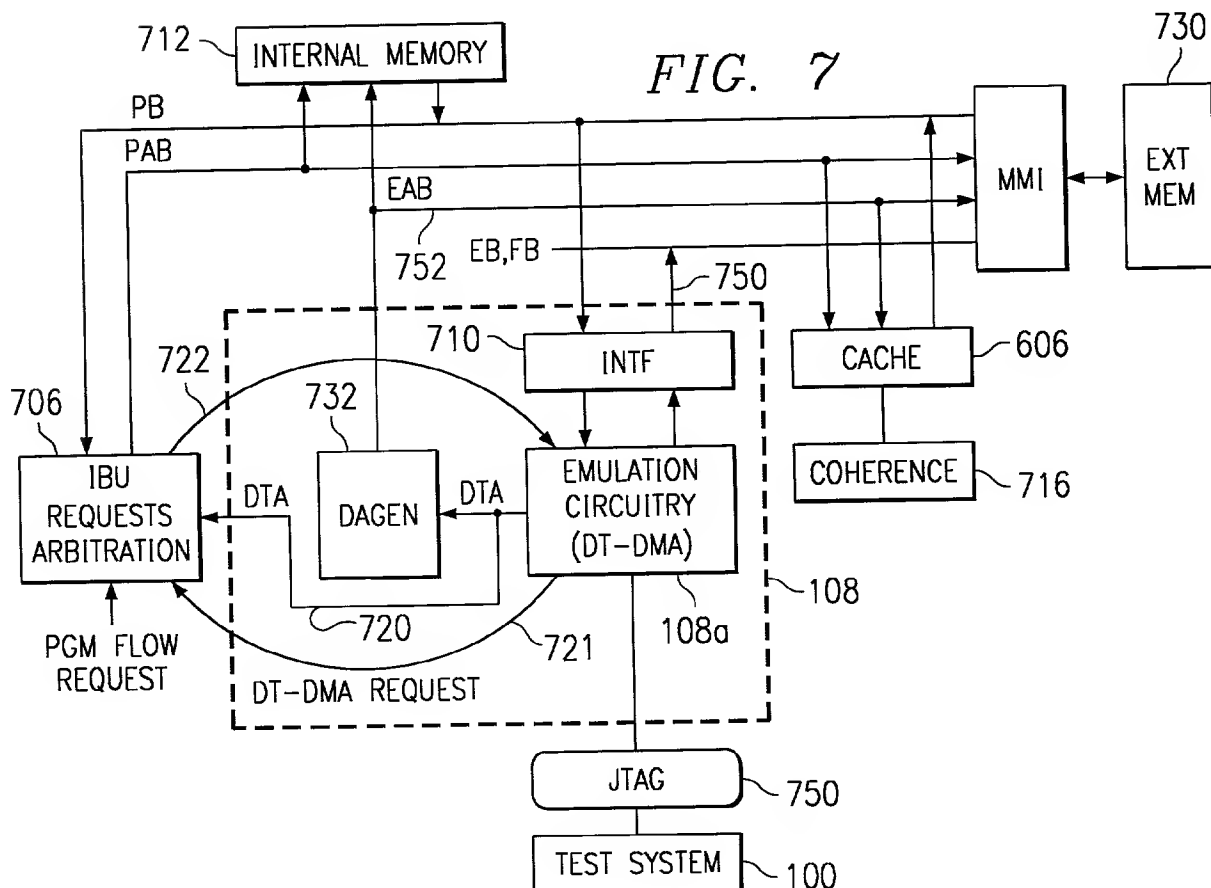


FIG. 7



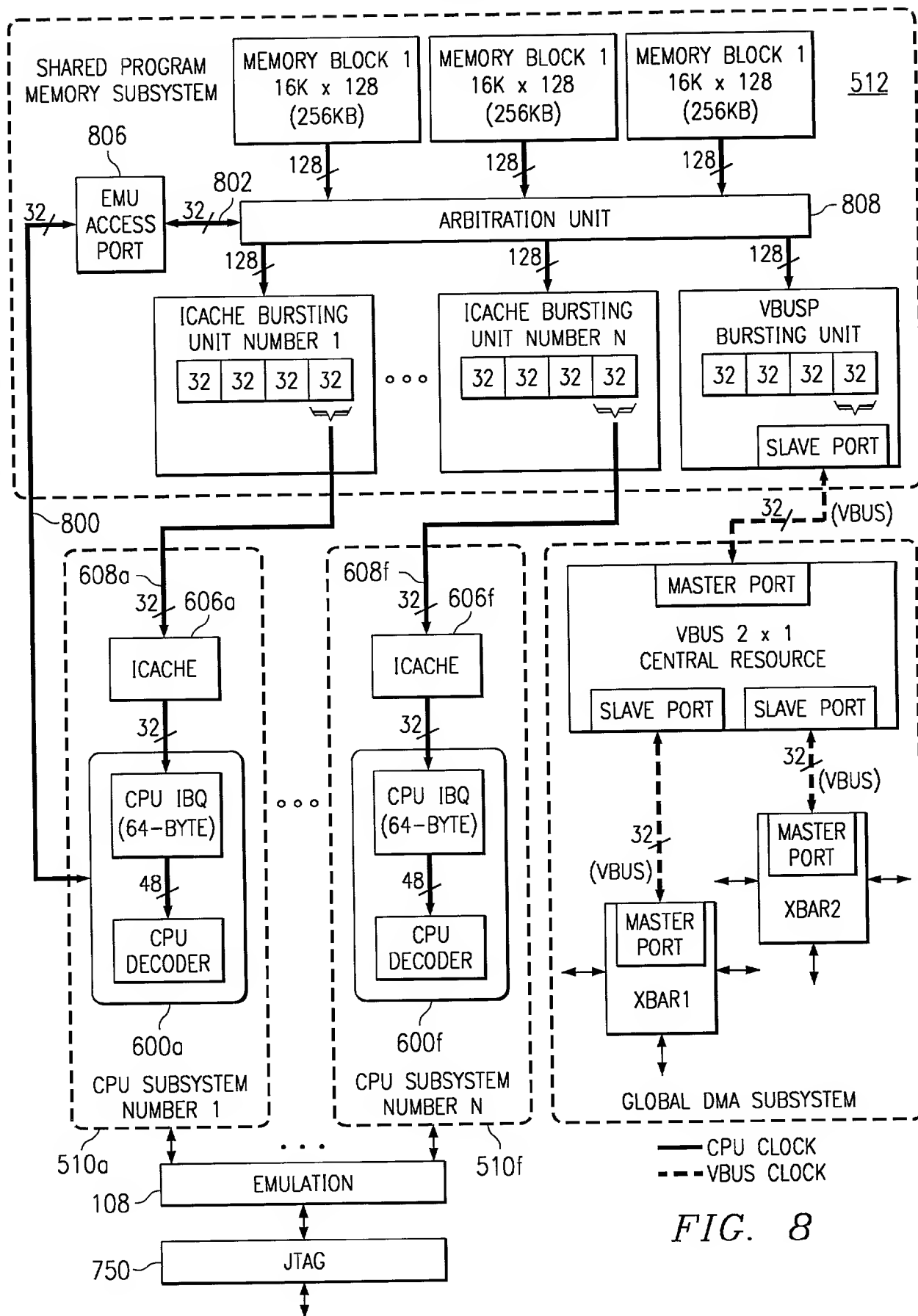
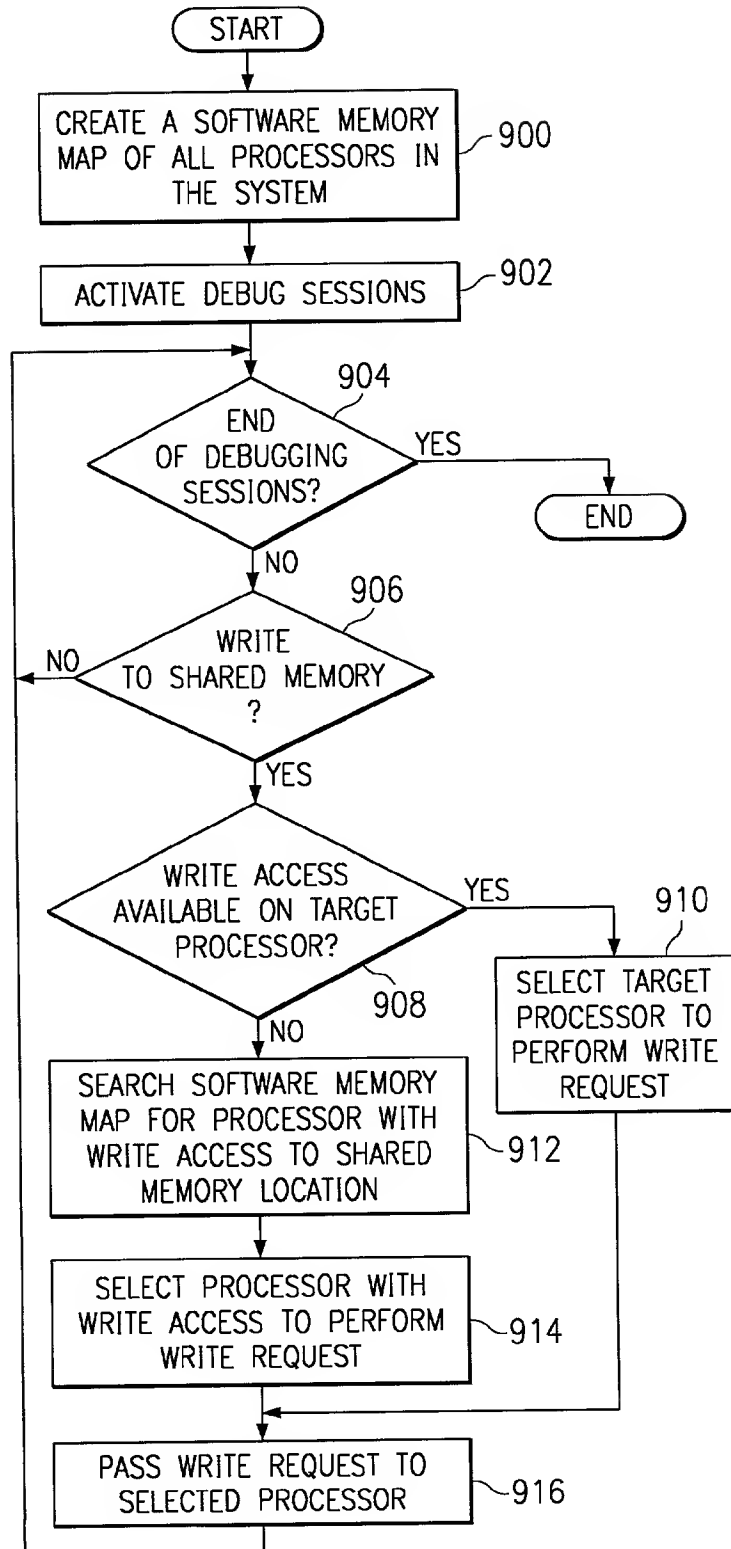


FIG. 9



BYTE ADDRESS

000000h	LOCAL MEMORY
00009Fh	MAPPED REGISTERS
0000A0h	LOCAL DARAM
00FFFFh	(64KBYTE) DATA
010000h	LOCAL SARAM
01FFFFh	(64KBYTE) DATA/PROG
020000h	LOCAL SARAM
06FFFFh	(320KBYTE) DATA
070000h	RESERVED
F3FFFFh	SHARED SARAM
F40000h	(768KBYTE) PROG
1000	LOCAL ROM (4KBYTE)
FFF000h	
FFFFFFh	

FIG. 10

FIG. 11

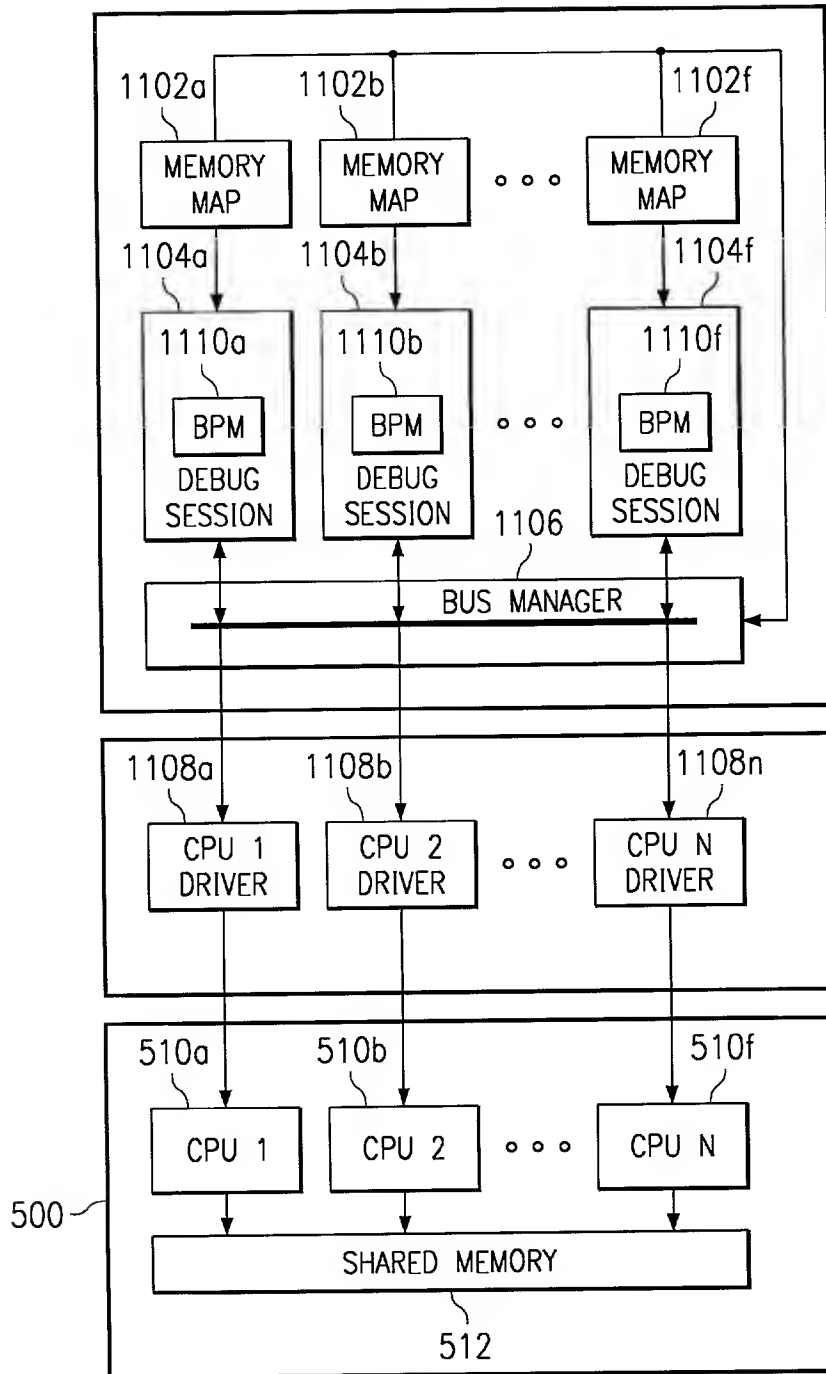


FIG. 12A

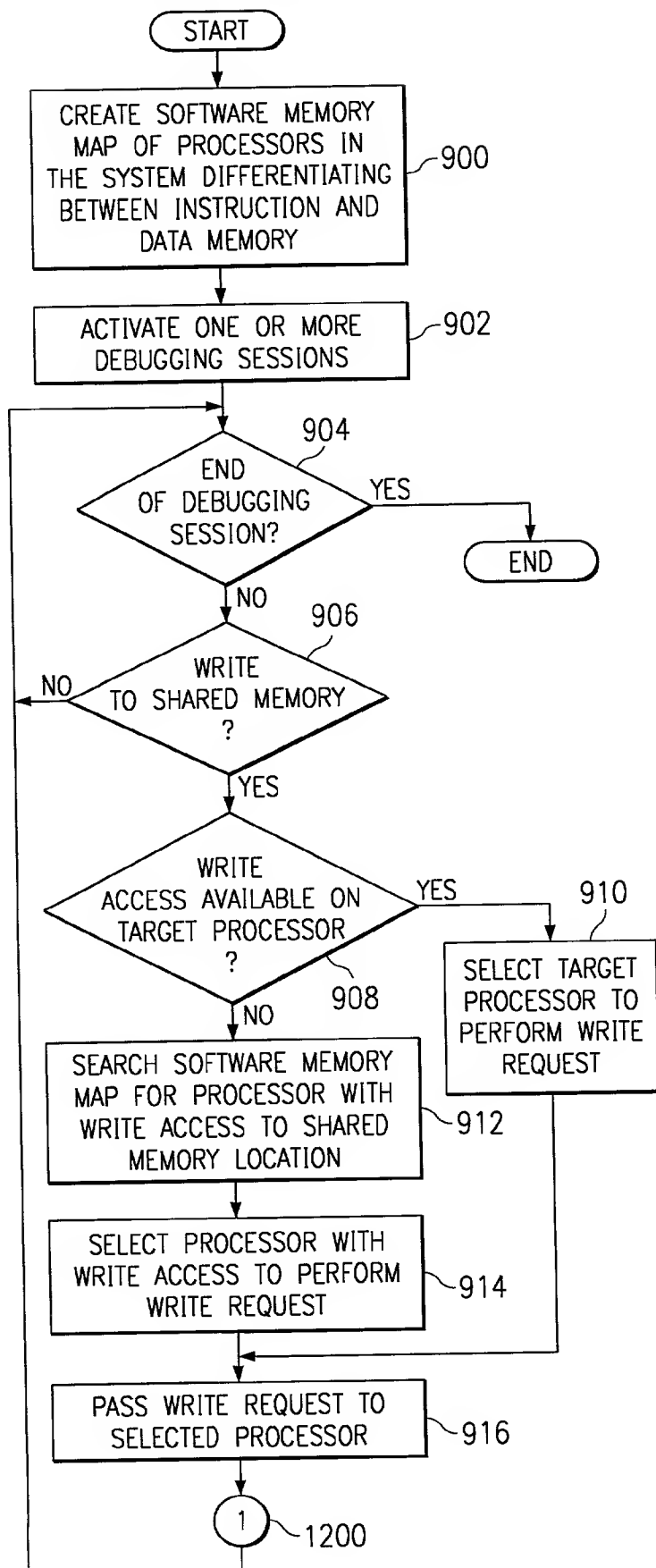


FIG. 12B

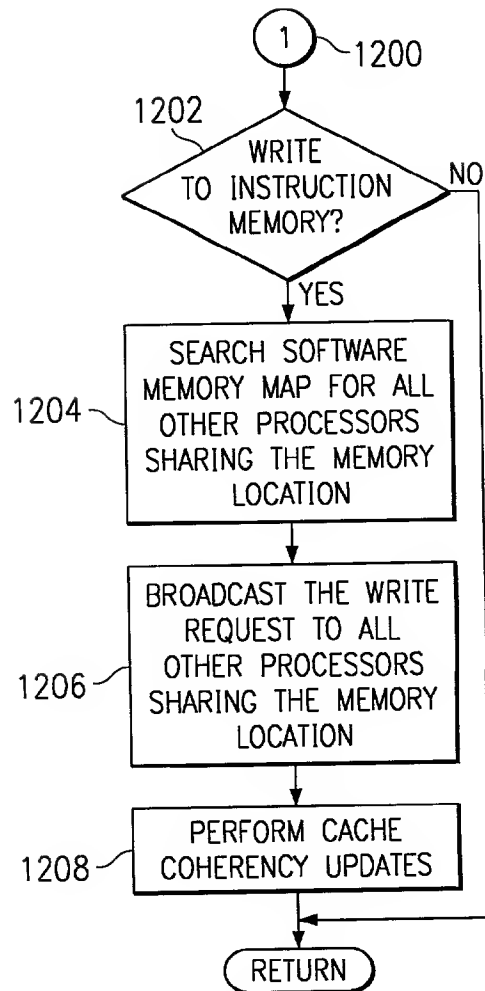


FIG. 13A

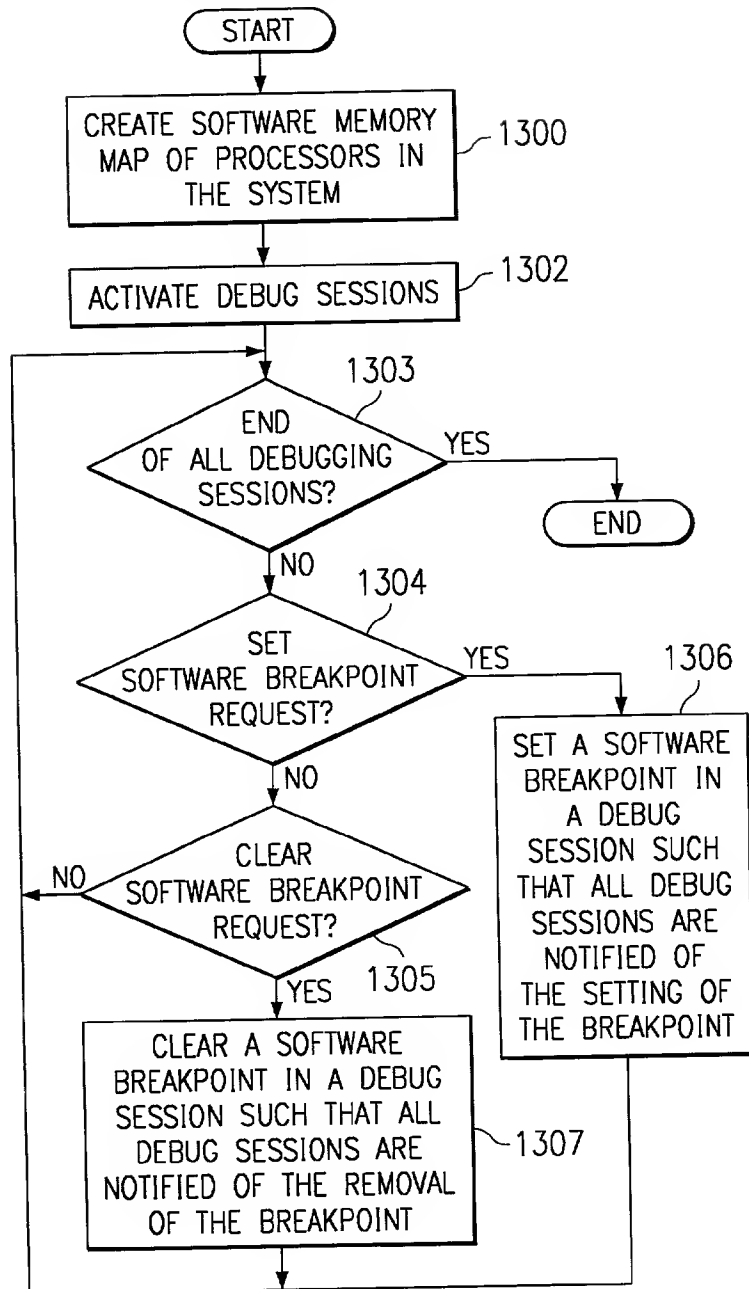


FIG. 13B

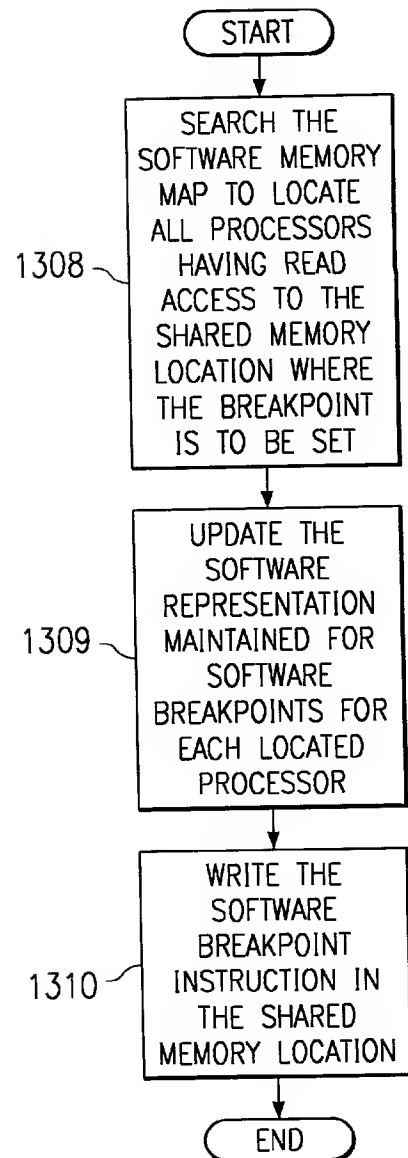
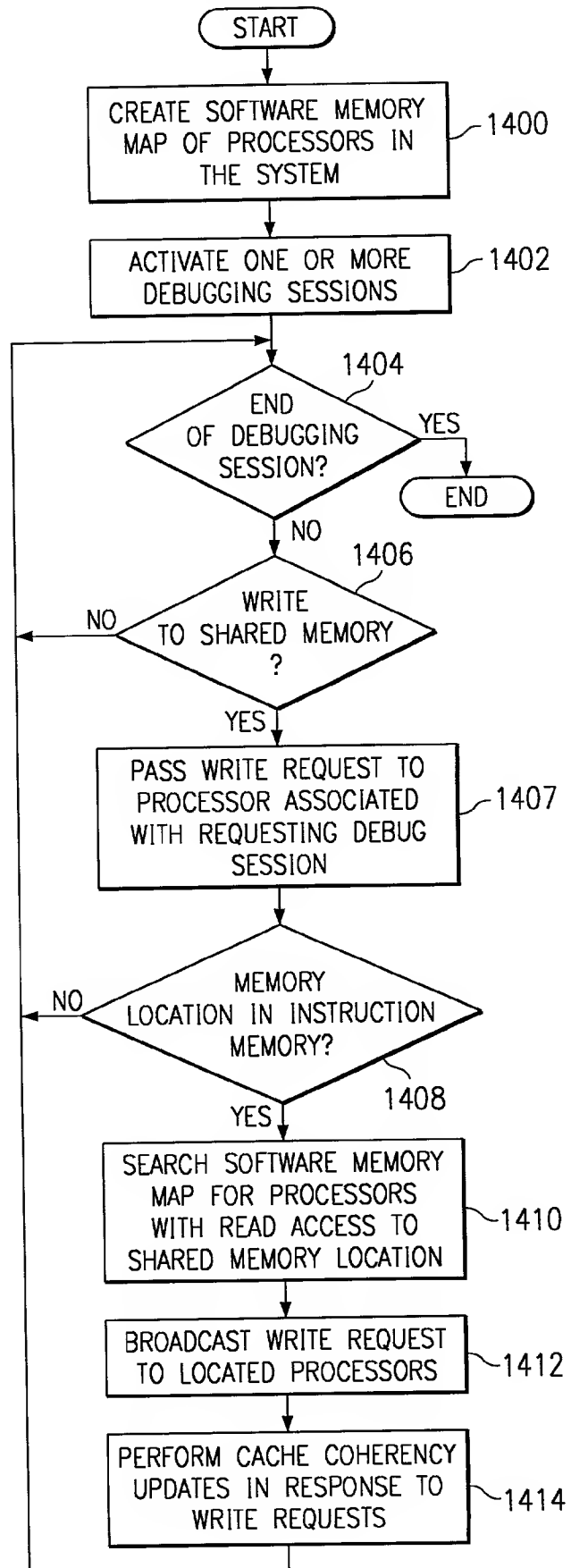
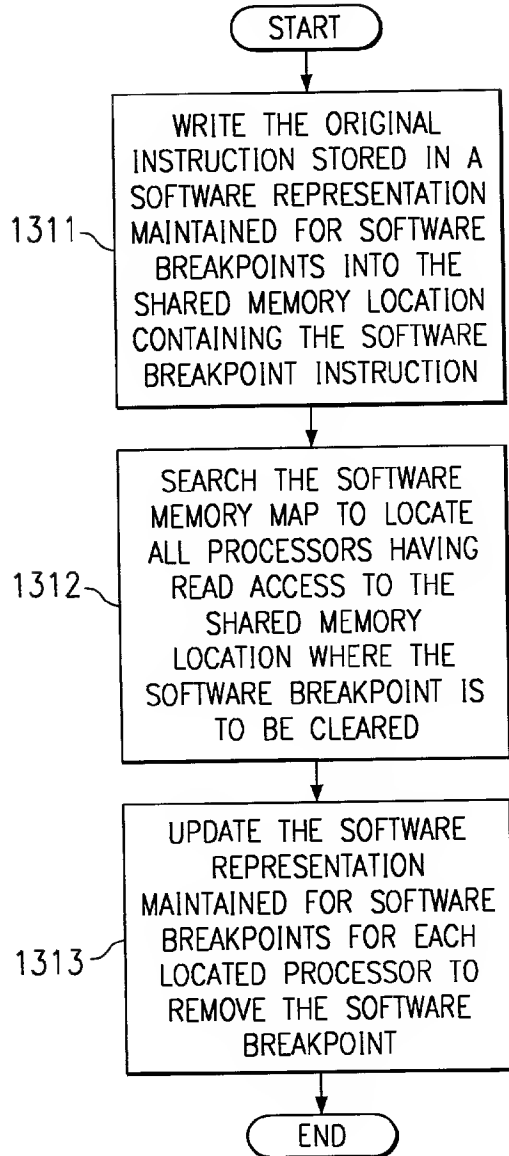


FIG. 13C



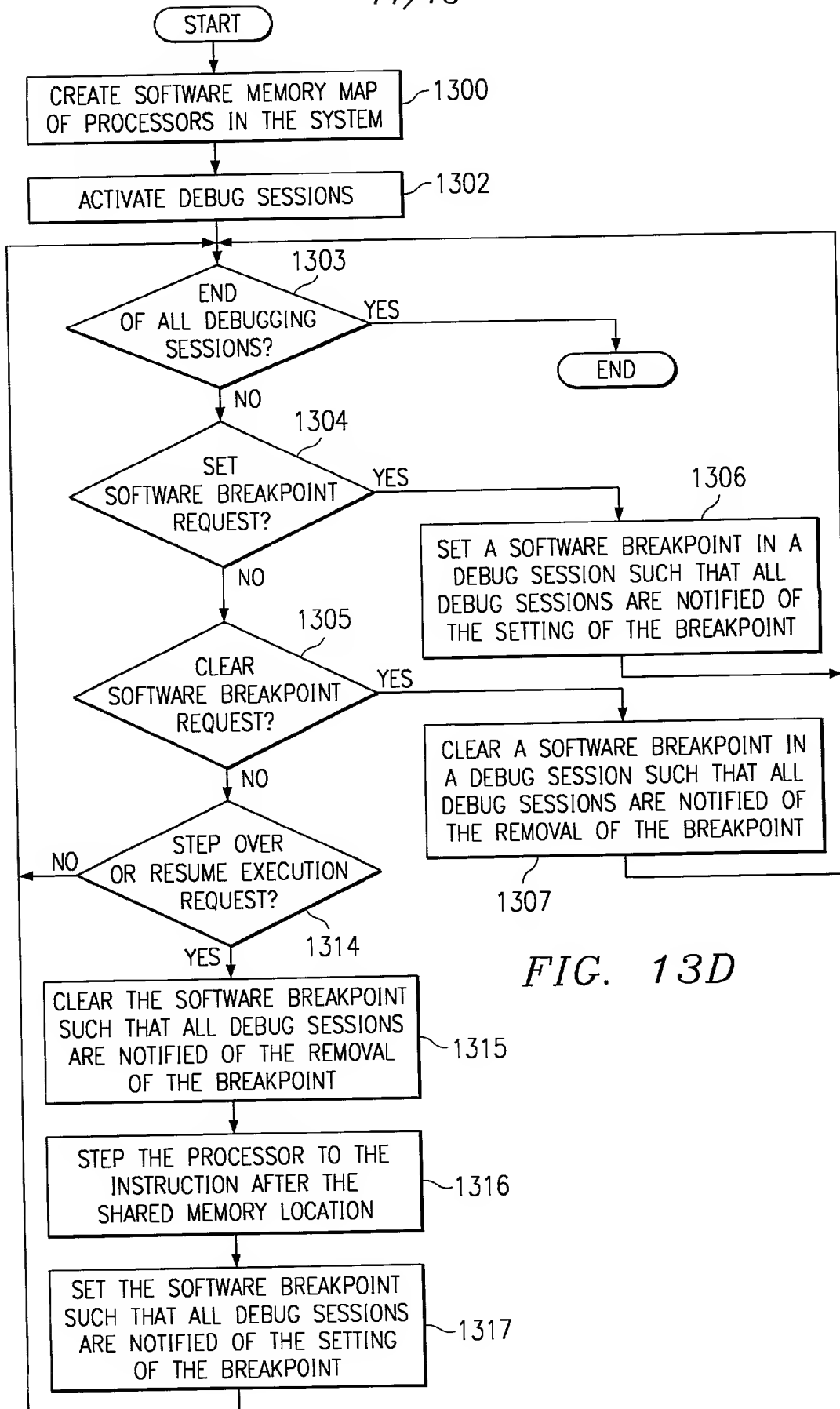


FIG. 13D

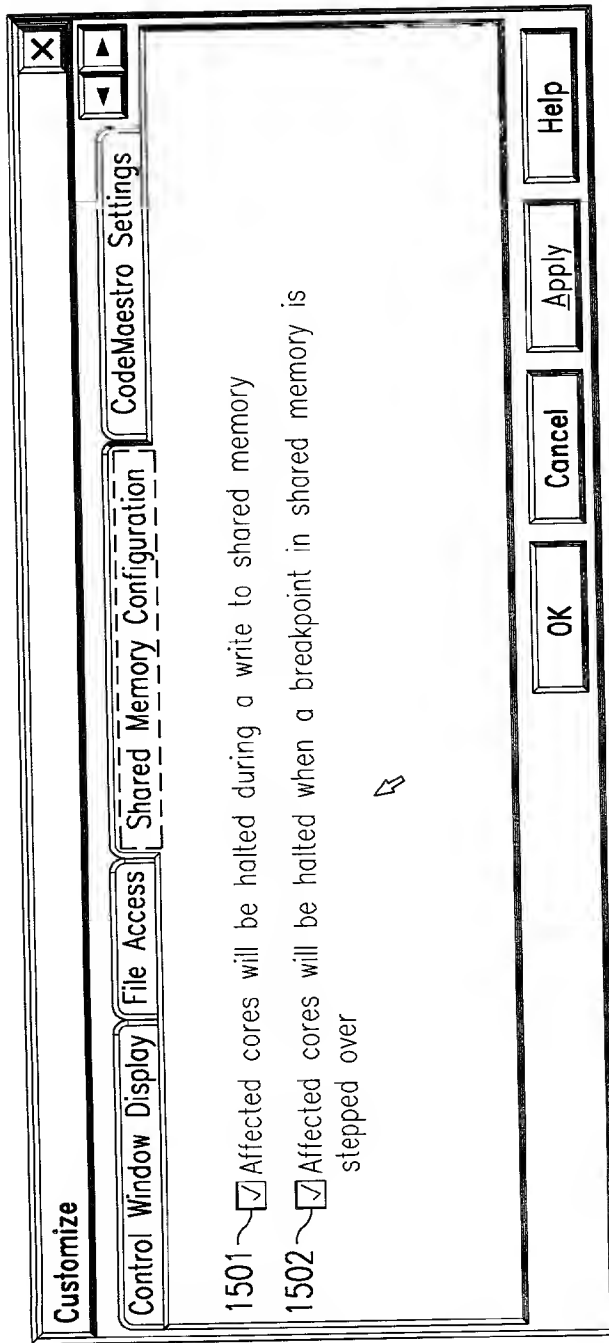


FIG. 15

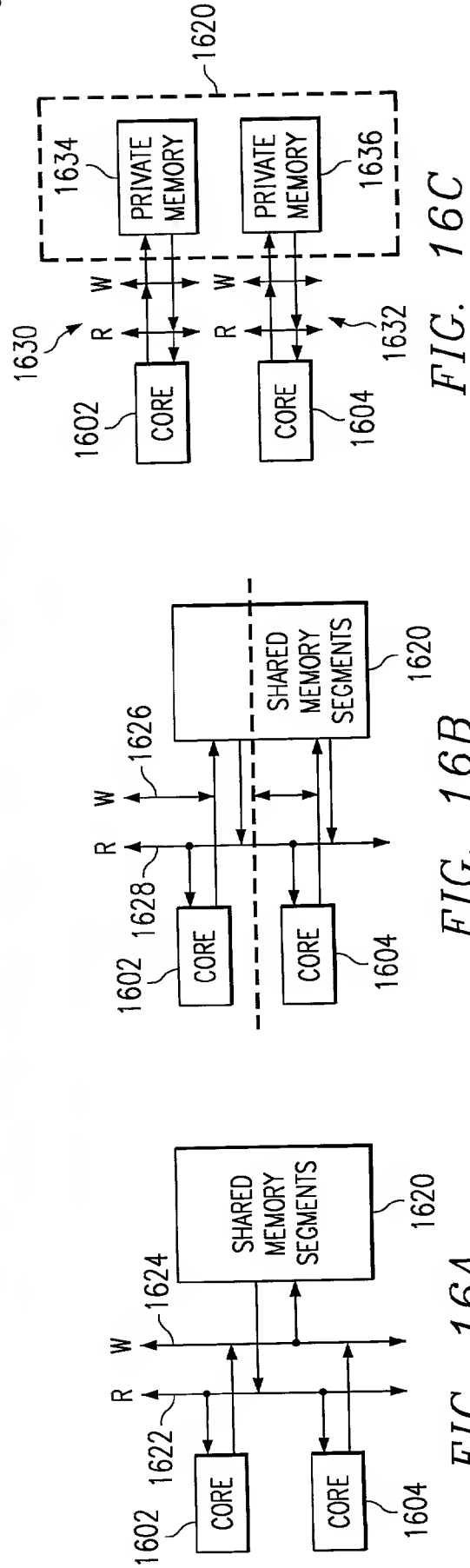


FIG. 17

